

<b>INFORMATION DISCLOSURE CITATION</b>  <b>PTO-1449</b>				<i>Complete If Known</i>	
				<b>Application Number</b>	10/699,116
				<b>Filing Date</b>	October 31, 2003
				<b>First Named Inventor</b>	Donnelly, et al.
				<b>Art Unit</b>	2189
				<b>Examiner Name</b>	Chace, Christian
				<b>Attorney Docket Number</b>	60809-0143-US
Sheet	1	of	1		

## **U.S. PATENT DOCUMENTS**

## **FOREIGN PATENT DOCUMENTS**

Examiner Initials	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Translation	
	Country Code <sup>2</sup> - Number <sup>3</sup> - Kind Code <sup>4</sup> (if known)					Yes	No
						X	

## **OTHER NON PATENT LITERATURE DOCUMENTS**

Examiner Initials	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published

Examiner Signature		Date Considered	5/6/05
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WIPO Standard ST. 16 if possible.<sup>5</sup> Applicant is to place a check mark here if English language Translation is attached.

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1

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### Complete if Known

Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2187
Examiner Name	Chace, C.

Attorney Docket Number RAMB-0/065US1

### U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-5742840	04-21-1998	Hansen et al.	
		US-5748872	05-05-1998	Norman	
		US-5860080	01-12-1999	James et al.	
		US-5867422	02-02-1999	John	
		US-5898863	04-27-1999	Ofer et al.	
		US-6006318	12-21-1999	Hansen et al.	
		US-6034878	03-07-2000	Osaka et al.	
		US-6038682	03-14-2000	Norman	
		US-6092229	07-18-2000	Boyle et al.	
		US-6125419	09-26-2000	Umemura et al.	
		US-6151648	11-21-2000	Haq	
		US-6154826	11-28-2000	Wulf et al.	
		US-6160423	12-12-2000	Haq	
		US-6255859	07-03-2001	Haq	
		US-6263413	07-17-2001	Motomura et al.	
		US-6327205	12-04-2001	Haq	
		US-6408402	06-18-2002	Norman	
		US-6487102	11-26-2002	Halbert et al.	

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Examiner Signature	Date Considered
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## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

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Sheet 2

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**Complete if Known**

Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2187
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

Attorney Docket Number **RAMB-01065US1**

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		Country Code <sup>3</sup> , Number <sup>4</sup> , Kind Code <sup>5</sup> (if known)				

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				Application Number	10/699,116
				Filing Date	October 31, 2003
				First Named Inventor	Donnelly
				Art Unit	2187
				Examiner Name	Chace, C.
Sheet <b>3</b>		of	<b>4</b>	Attorney Docket Number	<b>RAMB-01065US1</b>

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
		*U.S. Patent Application Serial No. 10/713,868, filed 11-14-2003 by Pete D. Vogt		
		*U.S. Patent Application Serial No. 10/714,025, filed 11-14-2003 by Pete D. Vogt		
		*U.S. Patent Application Serial No. 10/714,026, filed 11-14-2003 by Pete D. Vogt		
		*U.S. Patent Application Serial No. 10/883,474, filed 06-30-2004 by Pete D. Vogt		
		*U.S. Patent Application Serial No. 10/882,999, filed 06-30-2004 by Pete D. Vogt		
		*U.S. Patent Application Serial No. 10/859,438, filed 05-31-2004 by Pete D. Vogt		
		*U.S. Patent Application Serial No. 10/858,850, filed 05-31-2004 by Pete D. Vogt		
		*U.S. Patent Application Serial No. 10/859,060, filed 05-31-2004 by Pete D. Vogt		
		NG, R. (Sun Microsystems Inc.), "Fast computer memories", IEEE Spectrum, October 1992, pp. 36-39		
		SALTERS, R.H.W. (Philips Research Laboratories), "Fast DRAMS for sharper TV", IEEE Spectrum, October 1992, pp. 40-42		

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\*Please note that the copies of these references enclosed herewith were obtained from the file history for U.S. Patent Application Serial No. 10/456,353. Applicants' attorney assumes that they are the correct copies.

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Substitute for form 1449B/PTO		<b>Complete If Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		<i>Application Number</i>	10/699,116
(Use as many sheets as necessary)		<i>Filing Date</i>	October 31, 2003
		<i>First Named Inventor</i>	Donnelly
		<i>Art Unit</i>	2187
		<i>Examiner Name</i>	Chace, C.
Sheet	4	of	4
		<i>Attorney Docket Number</i>	RAMB-01065US1

## **NON PATENT LITERATURE DOCUMENTS**

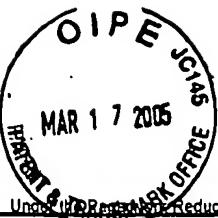
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1

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### Complete if Known

Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2187
Examiner Name	Chace, C.

Attorney Docket Number RAMB-01065US1

### U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US-6016282	1-18-2000	Keeth	
		US-6449727	09-10-2002	Toda	
		US-4644532	02-17-1987	George et al.	
		US-4667305	05-19-1987	Dill et al.	
		US-4747100	05-24-1988	Roach et al.	
		US-4864563	09-05-1989	Pavey et al.	
		US-5228134	07-13-1993	MacWilliams et al.	
		US-5355467	10-11-1994	MacWilliams et al.	
		US-5408646	04-18-1995	Olnowich et al.	
		US-5537394	07-16-1996	Abe et al.	
		US-5544342	08-06-1996	Dean	
		US-5553266	09-03-1996	Metzger et al.	
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		US-5604735	02-18-1997	Levinson et al.	
		US-5630095	05-13-1997	Snyder	
		US-5659710	08-19-1997	Sherman et al.	
		US-5787083	07-28-1998	Iwamoto et al.	
		US-5802054	09-01-1998	Bellenger	

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		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
		WO 99/30240	06-17-1999	Intel Corporation		
		WO 99/41666	08-19-1999	Intel Corporation		
		WO 99/41667	08-19-1999	Intel Corporation		
		EP 1069509 A2	01-17-2001	Texas Instruments Incorporated		

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Sheet 2

of 7

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Art Unit	2187
Examiner Name	Chace, C.

Attorney Docket Number RAMB-01065US1

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Examiner Initials*	Cite No. <sup>1</sup>	Document Number <small>Number-Kind Code<sup>2</sup> (if known)</small>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-5802565	09-01-1998	McBride	
		US-5805798	09-08-1998	Keams et al.	
		US-5848145	12-08-1998	Gallagher et al.	
		US-5884036	03-16-1999	Haley	
		US-5893921	04-13-1999	Bucher et al.	
		US-5900017	05-04-1999	Genduso et al.	
		US-5901294	05-04-1999	Tran et al.	
		US-5911052	06-08-1999	Singhal et al.	
		US-5913044	06-15-1999	Tran et al.	
		US-5923893	07-13-1999	Moyer et al.	
		US-6247100	06-12-2001	Drehmel et al.	
		US-5953215	09-14-1999	Karabatsos et al.	
		US-6142830	11-07-2000	Looeffler	
		US-6185644	02-06-2001	Farmwald et al.	
		US-6266252	07-24-2001	Karabatsos	
		US-6317352	11-13-2001	Halbert et al.	
		US-6414904	07-02-2002	So et al.	
		US-6446158	09-03-2002	Karabatsos	

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Sheet 3

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		Number-Kind Code <sup>2</sup> ( <i>if known</i> )			
		US-6493250	12-10-2002	Halbert et al.	
		US-6530006	03-04-2003	Dodd et al.	
		US-6449679	09-10-2002	Ryan et al.	
		US-6108731	08-22-2000	Suzuki et al.	
		US-5701313	12-23-1997	Purdham	
		US-5513135	04-30-1996	Dell et al.	
		US-6065092	05-16-2000	Roy	
		US-5034917	07-23-1991	Bland et al.	
		US-4977498	12-11-1990	Rastegar et al.	
		US-5068650	11-26-1991	Fernandez et al.	
		US-4947257	08-07-1990	Fernandez et al.	
		US-6643752	11-04-2003	Donnelly et al.	
		US-6690726	02-10-2004	Yavits et al.	
		US-5325493	06-28-1994	Herrell et al.	
		US-5867180	02-02-1999	Katayama et al.	
		US-6104417	08-15-2000	Nielsen et al.	
		US-6208273	03-27-2001	Dye et al.	
		US-5883839	03-16-1999	Tosaka et al.	

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 4

of 7

Complete if Known

Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2187
Examiner Name	Chace, C.

Attorney Docket Number RAMB-01065US1

### U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number <small>Number-Kind Code<sup>2</sup> (<i>if known</i>)</small>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-20040260991	12-23-2004	Vogt et al.	
		US-20040250181	12-09-2004	Vogt et al.	
		US-20040250153	12-09-2004	Vogt	
		US-20040150024	12-09-2004	Vogt	
		US-20040246786	12-09-2004	Vogt	
		US-20040246785	12-09-2004	Vogt	
		US-20040246767	12-09-2004	Vogt	
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		US-20020083287	06-27-2002	Zumkehr et al.	
		US-			

### FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document <small>Country Code<sup>3</sup> Number<sup>4</sup> King Code<sup>5</sup> (<i>if known</i>)</small>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>

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				Application Number	10/699,116
				Filing Date	October 31, 2003
				First Named Inventor	Donnelly
				Art Unit	2187
				Examiner Name	Chace, C.
Sheet	5	of	7	Attorney Docket Number	RAMB-01065US1

NON PATENT LITERATURE DOCUMENTS				
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		CATALDO, A., "TI Fields DSP-Based Media Processor on a DIMM", EE Times (Jan. 2000)		
		MAC WILLIAMS, P., "PC Memory Directions For 1999 and Beyond", Intel Developer Forum, pp. 1-10 (Sep. 1998)		
		JOHNSON, B., "Direct RDRAM Basic Architecture and Program Overview", Intel Developer Forum, pp. 1-14 (Sep. 1998)		

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				Filing Date	October 31, 2003
				First Named Inventor	Donnelly
				Art Unit	2187
				Examiner Name	Chace, C.
Sheet	6	of	7	Attorney Docket Number	RAMB-01065US1

NON PATENT LITERATURE DOCUMENTS						
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		Minutes of meeting no. 75 JEDEC JC-42.3 Committee on RAM Memories, and referenced attachment. "Y" of the presentation titled "SyncLink", May 24, 1995, New Orleans				
		Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Server Memory Architecture: Capacity, Performance, Reliability, and Longevity", February 18, 2004				
		Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Design Considerations", February 18, 2004				

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